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A FPGA Based PI Adaptive Sliding Mode Controller for PEM Fuel Cell with Boost Converter

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https://doi.org/10.14447/jnmes.v25i4.a01 ABSTRACT

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Keywords:

FPGA; fuel cell; boost converter; PI adaptive sliding mode control; uncertainty

Proton exchange membrane (PEM) is one of the most popular fuel cells for renewable energy production, and this paper presents a DC/DC boost converter structure to improve energy efficiency. To achieve high output power as well as constant output voltage, a combined method consisting of proportional-integral controller and adaptive sliding mode technique is designed. Also, due to the use of adaptive technique, the proposed technique has the ability to cover the effects of uncertainty with an unknown high limit and in addition to improving the quality of output power and voltage of PEM, it eliminates permanent tracking error and guarantees closed loop system stability. To show the operational implementation capability and flexibility of the method, FPGA has been used, which in addition to showing real-time performance, provides the ability to execute the controller at high speeds. Operationality and the possibility of practical implementation of the planned scheme on the existing systems provide the possibility of increasing the efficiency of energy extraction without further investment. The stability of the closed-loop system is achieved using Lyapunov technique and the results of simulation and comparison indicate the optimal performance of the system under the planned scheme and high efficiency in comparison with existing approaches.

1. INTRODUCTION

Energy is the most important need and subject of research in today's world [1-4], and many researches are being carried out in the field of different techniques of its production from different sources [5-6], proper storage [7-8], its use in various applications [9-10], and its management and control [11-12]. Efforts to prevent devastating changes in climate and the environment have led to modifications in the energy sector, and achieving global goals in this area is possible with the improvement of low-carbon energy expertise, especially with the use of renewable energy. The energy sector is the main producer of greenhouse gases and the main cause of global warming, and change in the energy production system is seen as the key to achieving a cleaner and safer future. Renewable energies, in addition to being clean, are abundant and reliable, and if properly developed, can play an important role as a sustainable energy source in meeting the needs and reducing climate change [13-16].

One of the most significant expertise being developed for the generation of renewable energy is fuel cells. These devices operate on the principles of electrochemistry, and one of the most prevalent kinds is the proton exchange membrane fuel cell (PEMFC). This tools provides a safe, clean and efficient energy source with a long life and costs much less than other fuel cell technologies [17]. In addition, PEM has a wide variety of applications in transportation, vehicles and distributed power generation because of its high power density, low temperature in service and high conversion efficiency. The DC/DC converters are often used to convert the output power of the fuel cell to the appropriate usable power. There are various techniques in using converters, the most popular of which are buck converters (voltage reducers), boost converters (voltage boosters) and other types of converters such as buckboost and Cuk [18]. In these converters, DC output voltage is controlled through pulse width modulation (PWM) technique.

To achieve greater efficiency and transfer as much power as possible from the fuel cell stack to the load, different control techniques have been used to ensure that the system operates at the maximum power point. Factors such as nonlinearity of the fuel cell system, uncertainty in the model, and disturbances seriously affect the performance of classical linear control approaches for DC/DC converters. For this reason, recently advanced techniques such as feedback linearization [19], fuzzy logic controller (FLC) [20, 21] and sliding mode controller (SMC) [22-25] have been widely considered in articles. Artificial neural network (ANN) and particle swarm optimization (PSO) techniques have also been used to increase the efficiency of control approaches. Sliding mode controller has been used in various fields due to several advantages such as the ability to stabilize the system against changes in the converter parameters and load disturbances [26-28], ease of operation and excellent performance [29-30]. However, applying a sliding mode technique in a DC-DC converter causes some major difficulties including: 1. Change the switching frequency and create low-order harmonic components; 2. Steady state error; 3. The overall system instability resulting from a right zero in the voltage transfer function of DC-DC boost converter; 4. Finally, in addition to not considering the effects of parasitic and destructive elements on the converter model, the possibility of practical implementation of the proposed method has been completely neglected.

To overcome the stated issues and getting the maximum output power from the PEM fuel cell, this paper schemes a robust indirect technique for controlling the PEM output voltage with the possibility of its practical implementation. Using proportional-integral (PI) control method, sliding mode and adaptive techniques, a hybrid scheme has been planned to control the output voltage of PEM fuel cell, which in addition to stabilizing the system in the operating range, is able to cover parametric uncertainty effects. The proposed innovative approach is implemented using Field Programmable Gate Arrays (FPGAs) which is used as a suitable platform for embedded control systems and provides a suitable hardware substrate with advantages such as high execution speed, design Offers, high flexibility and reliability. It also allows the designer to test and validate the control algorithm and optimize its performance in less time and at less cost without real and practical implementation. Given the advantages of FPGA in various areas of performance, reliability, long-term maintenance, cost and time to market, this paper presents a new hybrid control method implemented through FPGA to improve the energy efficiency of PEM fuel cells.

So, the paper has been compiled as follows. The second part presents the PEM fuel cell model equipped the DC-DC converter. The third section describes the planned FPGA based controller configuration. In the fourth section, the results of simulations and comparisons have given by using of Toolbox FPGA. Finally, the fifth part is devoted to conclusions and future proposals.

2. MODEL OF FUEL CELL SYSTEM MODEL WITH DC-DC BOOST CONVERTER

This part describes the fuel cell system model with DC-DC boost converter. The arrangement of static and dynamic representations of PEM fuel cell in company with the general model of fuel cell with converter are given in three sections.

2.1 The Static Model of PEM Fuel Cell

Converting chemical energy into electrical energy is the main task and function of PEM fuel cell, so its overall plan is shown in Figure 1.

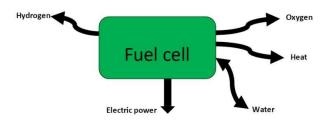


Figure 1. Overall plan of PEM fuel cell

In the energy conversion process inside the fuel cell, ordinary water and heat are produced. The different parts of the fuel cell, containing anode, cathode and membrane, are presented in Figure 2. The oxidation reaction inside the fuel cell is as follows:

$$2H_2 \Longrightarrow 4H^+ + 4e^- \tag{1}$$

The free H^+ ions pass through the membrane, while the e⁻-ions cannot. The subsequent reduction reaction takes place inside the cathode as well.

$$0_2 + 4H^+ + 4e^- \Longrightarrow 2H_20 \tag{2}$$

In reaction (2), oxygen entering the cathode reacts with H^+ moving from side to side the membrane and e^- is also transferred to the surface of the cathode with an external load. The overall reaction is as follows:

$$2H_2 + O_2 \Longrightarrow 2H_2O + \text{electrical energy}$$
(3)

Therefore, the output voltage of single fuel cell (V_Fc) and stack fuel cell (V_stack) will follow the relations (4) and (5).

$$V_{Fc} = E_{Cell} - \eta_{act} - \eta_{ohm} - \eta_{con} \tag{4}$$

$$V_{stack} = N_{cell} \cdot V_{Fc} = N_{cell} \cdot (E_{cell} - \eta_{act} - \eta_{ohm} - \eta_{con})$$
(5)

In the mentioned equations, E_Cell indicates the PEMFC electrochemical thermodynamics potential and it reports the best yield voltage, η_{act} specifies activation polarization loss and identifies the voltage drop due to the activation of the anode and the cathode, η_{ohm} indicates ohmic polarization loss resulting from membrane resistance to transfer protons and from electrical resistance of the electrodes to transfer electrons, η_{con} provides the loss of concentration polarization caused by the decrease in the concentration of the reactants, oxygen and hydrogen, which varies during the reaction and N_cell specifies the number of fuel cell in stack.

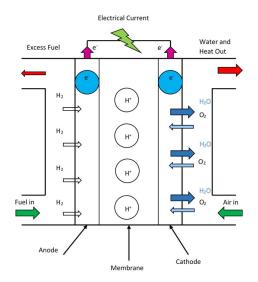


Figure 2. Different sections of PEMFC and their operation diagram

The dynamic model of PEMFC is given in this part. For the reason that the membrane is placed in the mid of two charged layers in the PEMFC, it works as a capacitor in point of fact. Since R_{ohm} , R_{act} , R_{con} are ohmic, activation and the

concentration resistances respectively, the PEMFC equivalent circuit is given as in Figure 3.

By considering the Kirchhoffs Voltage Law (KVL) and Kirchhoffs Current Law (KCL) for the equivalent circuit, it is achieved:

$$V_{cell} = E_{cell} - V_d - IR_{ohm} \tag{6}$$

$$I = C \frac{dV_d}{dt} + \frac{V_d}{R_{con} + R_{act}}$$
(7)

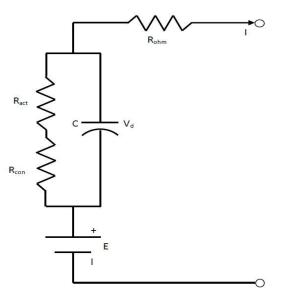


Figure 3. The equivalent circuit of PEMFC

Equation (7) can be rephrased in the following way:

$$\frac{dV_d}{dt} = \frac{1}{C}I - \frac{1}{\tau}V_d \tag{8}$$

Where V_d states the voltage through the equivalent capacitor, *C* is the corresponding capacitance, and τ specifies the fuel cell time constant as following:

$$\tau = C(R_{act} + R_{con}) = C\left(\frac{\eta_{act} + \eta_{con}}{I}\right)$$
(9)

By inserting equations (9) and (8) into equation (6) and taking the Laplace transform, it is obtained:

$$V_{cell} = E_{cell} - \left(\frac{R_{act} + R_{con}}{(R_{act} + R_{con})C.s + 1} + R_{ohm}\right)I \tag{10}$$

To describe the behavior of PEM more accurately, the pressure of hydrogen and oxygen gases as well as their flow should be included in the dynamic model. According to references [31-32], partial pressure equations for oxygen and hydrogen are as follows:

$$P_{H_2} = \frac{1/K_{H_2}}{(1+\tau_{H_2})} (q_{H_2} - 2I.K_r) 2H_2$$
(11)

$$\Rightarrow 4H^{+} + 4e^{-}$$

$$P_{o_{2}} = \frac{1/K_{o_{2}}}{(1+\tau_{o_{1}})} (q_{o_{2}} - I.K_{r})$$
(12)

Where:

$$\begin{cases} \tau_{H_2} = \frac{V_{an}}{R.T.K_{H_2}} \\ \tau_{O_2} = \frac{V_{an}}{R.T.K_{O_2}} \end{cases}$$
(13)

 K_{H_2} is the valve molar constant of hydrogen (*kmol/s atm*), K_{O_2} means the valve molar constant of oxygen (*kmol/s atm*), q_{H_2} specifies the flow rate of hydrogen, q_{O_2} is the oxygen flow rate, τ_{H_2} indicates the response time of hydrogen (s), τ_{O_2} is the response time of oxygen (s), K_r means a modeling parameter constant (*kmol/(sA*)), and V_{an} is the anode volume. By means of the mentioned equations, the PEMFC system can be arranged as Figure 4.

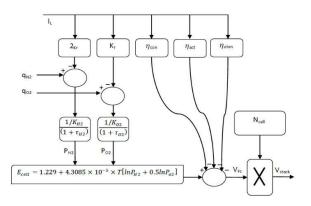


Figure 4. Dynamic model of PEMFC system

2.2 The State Space Model of DC/DC Boost Converters

The duty of the PEMFC DC/DC booster converter is to increase the voltage level and its plan is given in Figure 5.

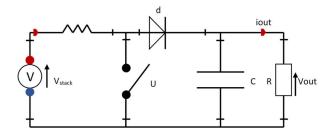


Figure 5. The plan of PEMFC DC/DC booster converter

This plan includes a PEM as a voltage source, a transistor switch, a diode switch, a filter capacitor, an inductance and an ohmic load. Through the u switch, the output follows the subsequent relationship

$$V_{out} = \left(\frac{1}{1-u}\right) \cdot V_{stack} \tag{14}$$

The working modes of this boost converter are in two ways, in the first mode, when u is on and d is off, the dynamic equations of the inductance current and output voltage are like so:

$$\begin{pmatrix} \frac{di_L}{dt} = \frac{1}{L}(V_{stack}) \\ \frac{dV_{out}}{dt} = \frac{1}{C}(-i_{out}) \end{cases}$$
(15)

In the second operating manner, when u is off and d is on, the dynamic equations of inductance current and output voltage are like this:

$$\begin{cases} \frac{di_L}{dt} = \frac{1}{L} (V_{stack} - V_{out}) \\ \frac{dV_{out}}{dt} = \frac{1}{C} (i_L - i_{out}) \end{cases}$$
(16)

By defining inductance current and output voltage as state variables and also $V_{stack} = v$, the state space equations for the boost converter are:

$$\begin{cases} \dot{x} = \begin{pmatrix} 0 & \frac{u-1}{L} \\ \frac{1-u}{C} & -\frac{1}{RC} \end{pmatrix} x + \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} v \\ y = \begin{bmatrix} 0 & 1 \end{bmatrix} x \end{cases}$$
(17)

The parameters of PEMFC are given in Table 1. For more information about the model, the reader can refer to [31-35].

Table 1 . The parameters of PEMF	С
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Symbol	Symbol Define	Value	Unit
Eo	Reference	1.229	V
	potential (in volts)		
R	Gas constant	83.143	$J mol^{-1}K^{-1}$
F	Faraday constant	96485.309	$C mol^{-1}$
Т	membrane	298.15	K
	temperature used		
	in the tests (in		
	Kelvin)		
A	membrane active	162	cm ²
	area		
ψ	parametric	23	-
	coefficient, used		
	to model the		
	membrane		
	resistance (minimum 14,		
	maximum 23)		
1	membrane	175.10 ⁻⁶	<i>am</i>
l	thickness	175.10	ст
В	parametric	0.1	V
	coefficient, used	0.1	v
	in the calculation		
	of the		
	concentration		
	losses		
R _c	membrane	0.0003	Ω
	equivalent contact		
	resistance		
J _{max}	maximum current	0.062	$A \ cm^{-2}$
	density		
N _{cell}	number of FCs	10	-
	used in the stack		
ξ_1	$\xi_1 \dots \xi_4$ are	0.9514	V
	parametric		
	coefficients, based		
	on ala atua ah anaisa 1		
	electrochemical,		
L	kinetics, and		

	thermodynamic laws		
ξ_2		-0.00312	V/K
ξ_3		$-7.4.10^{-5}$	V/K
ξ_4		$1.87.10^{-4}$	V/K

3. PLANNED FPGA BASED PI ADAPTIVE SLIDING MODE CONTROLLER

The planned control scheme is defined in this part. In the plan of the control technique, a number of problems have been reflected which have been neglected by previous studies: 1-Due to the non-minimum phase nature of the DC/DC boost converter, direct control approaches are not able to correctly control and extract the maximum power from the DC source, and of course this feature of the converter leads to controller error and instability of the overall system; 2- Like any other physical system, uncertainty in the system model is inevitable. These uncertainties occur in different parts of the system and sometimes their upper limit is not known; 3- The most important approach of this study is the possibility of implementing the planned technique through FPGA. This approach enables the practical realization of the planned technique and provides the possibility of its development to the existing systems.

To overcome the adverse effects stated above and of course the practical realization of the planned approach, this paper presents a new hybrid control skill based on PI, adaptive and sliding mode techniques with the possibility of implementation through FPGA. In order to achieve zero steady state error and extract maximum power from the fuel cell, the sliding mode scheme is combined with the PI technique. This approach guarantees zero error in the induced current and thus maximum power extraction. Also, by using the adaptive approach, the upper bound of uncertainties is estimated and the stability of the system is guaranteed in the conditions of uncertainty. Finally, the different parts of the controller are structured using the FPGA platform in the MATLAB environment. The general schematic of the controller is given in Figure 6.

Considering V_{stack} , i_{ref} , i_L and V_{out} , the planned scheme performs the necessary control action on the boost converter and determines the desired i_L and V_{out} . Now the control design process is described.

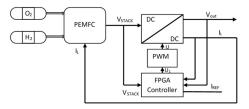
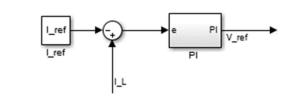


Figure 6. The general schematic of controller for PEMFC power system

By applying the PI technique, which is realized by the FPGA platform in Figure 7, the dynamics of the output voltage is defined as follows:



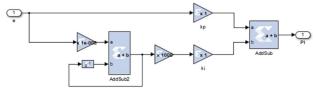


Figure 7. Realization of PI technique through FPGA platform

$$V_{ref} = k_P e + k_I \int e dt \tag{18}$$

Where k_P and k_I specify the PI controller gains and the state variables are described as

$$e_1 = i_L - i_{ref}$$
(19)
$$e_2 = V_{out} - V_{ref}$$

In relation to Equation (17), the dynamics of the error variables are

$$\dot{e}_{1} = \frac{1}{L}(V - V_{out}) + \frac{1}{L}u$$

$$\dot{e}_{2} = \frac{1}{C}\dot{i}_{L} - \frac{1}{R_{C}}V_{out} - \frac{k_{P}}{L}(V - V_{out})$$

$$- k_{I}(\dot{i}_{L} - \dot{i}_{ref}) - \left(\frac{1}{C} + \frac{k_{P}}{L}\right)u$$
(20)

Establishing the condition of stability by using the Lyapunov principle under the robust controller guarantees the condition of convergence of the system to the sliding surface. By selecting the sliding switching surface as

$$S = k_P(i_L - i_{ref}) + k_I \int (i_L - i_{ref}) dt$$
⁽²¹⁾

Fulfillment of the condition (22) for the sliding surface is required to achieve the control goal using the planned control scheme.

$$\frac{dS}{dt} = 0 \tag{22}$$

It is obtained by applying Equation (20) in Equation (22)

$$\dot{S} = k_P \left(\frac{1}{L} (V - V_{out}) + \frac{1}{L} u \right) + k_I (i_L - i_{ref})$$
(23)
= $\psi(.) + u$

Where

$$\psi(.) = k_{I} \left(i_{L} - i_{ref} \right) + \frac{k_{P}}{L} (V - V_{out}) + \frac{k_{P} - L}{L} u$$

$$(24)$$

The Lyapunov technique is used to ensure stability and find out the suitable control signal to realize the control objectives. The candidate Lyapunov function is chosen as equation (25).

$$V = \frac{1}{2}S^2 + \frac{1}{2}\tilde{\psi}^2$$
 (25)

Where $\tilde{\psi}$ indicates the error between Ψ and $\hat{\psi}$

$$|\psi(.)| < \Psi \tag{26}$$

Where Ψ states the unknown upper limit of $\psi(.)$ and $\hat{\psi}$ specifies the upper limit estimation. Derived from the Lyapunov function (25) provides

$$\dot{V} = S\dot{S} - \tilde{\psi}\dot{\hat{\psi}} \tag{27}$$

By inserting equation (23) in equation (27), we have

$$\dot{V} = S\psi(.) + Su - \tilde{\psi}\dot{\psi}$$
Now pick the control signal as follows: (28)

$$u = -k\,sign(S) - \hat{\psi} \tag{29}$$

It is now achieved by placing (29) in equation (28)

$$\dot{V} = -k \, S \, sign(S) + S(\psi(.) + \hat{\psi}) - \tilde{\psi}\hat{\psi} \qquad (30)$$

Consistent with Equation (40), Equation (44) can be modified as follows

$$\dot{V} \le -k|S| + S(\Psi - \hat{\psi}) - \tilde{\psi}\dot{\psi}$$
⁽³¹⁾

To find the adaptive rule, we modify Equation (31) as follows

$$\dot{V} \le -k|S| + \left|\tilde{\psi}\right| \left(S - \hat{\psi}\right) \tag{32}$$

Consistent with (32), to eliminate adaptive error $\tilde{\psi}$, its coefficient should be zero. For this reason, the adaptive law is defined as follows

$$\hat{\psi} = \lambda S \tag{33}$$

Where $\lambda > 1$ specifies the parameter of adaptive law regulation. The realization of this part of the controller by FPGA platform is shown in Figure 8.

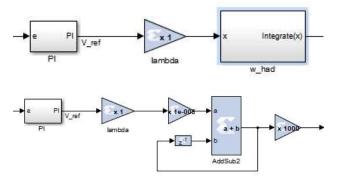


Figure 8. Realization of adaptive technique through FPGA platform

By placing equation (33) in (32), we have

$$\dot{V} \le -k|S| - (\lambda - 1)|S||\tilde{\psi}|$$
 (34)

Equation (34) confirms that by control signal of Equation (29) and through adaptive rule (33), the closed loop system is stable and the realization of control goals is guaranteed.

The complete implementation of the planned FPGA based control method is given in Figure 9. A connection between Simulink MATLAB and the FPGA platform is established through the system generator and input and output gate blocks. In the next part, simulation in MATLAB and FPGA environments will be used to accurately evaluate the capabilities of the controller.

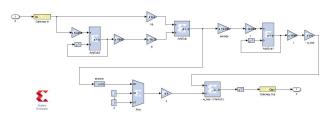


Figure 9. The complete implementation of the planned FPGA based control method

4. Numerical Simulation Results

The aim of this part is to apply the planned control scheme on the system involving the PEMFC and DC/DC boost converter and MATLAB simulation environment and FPGA platform have been used to assess its performance. The subsequent hardware is used for this purpose. Intel Core i7-10750H, 16GB DDR4 RAM, 512GB SSD, NVIDIA GeForce RTX 2060, 6GB GDDR6. Also, the results obtained from the application of the planned FPGA based scheme have been compared with the results from the implementation of supertwisting algorithm (STA) and SMC approaches from reference [36] in order to have a better analysis of the efficiency of the mentioned scheme. System parameters and operating conditions are all taken from [36].

As mentioned before, the aim of this study is the practical implementation of the controller through FPGA to extract the maximum possible power from PEMFC despite the nature of the non-minimum phase of the converter and also the presence of uncertainty in the load. For this purpose, extreme changes in the load according to Figure 10 are considered in the simulation. Also, according to [36], the current $i_l = 9.74 A$ is considered as the reference current to guarantee the optimal performance of the controller, because the maximum power extraction occurs at this load current value. Now the obtained results are explained.

The output voltage under FPGA based, SMC and STA controllers is shown in Figure 11. Figures 12 and 13 also show the voltage of one PEM cell and a PEM stack under load changes and with different control approaches.

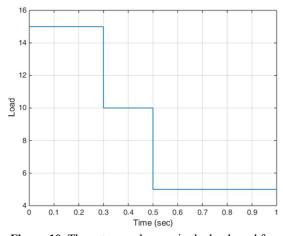


Figure 10. The extreme changes in the load used for simulation scenario

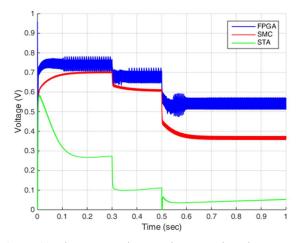


Figure 11. The output voltage under FPGA based, SMC and STA approaches

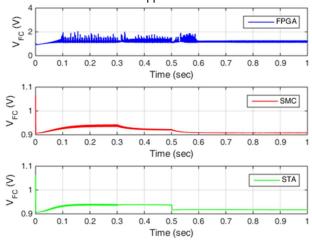


Figure 12. One PEM fuel cell voltage under FPGA based, SMC and STA approaches

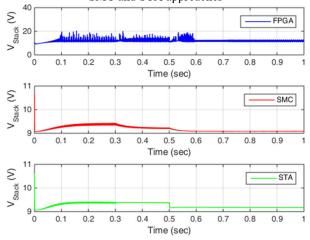


Figure 13. The PEM fuel cell stack voltage under FPGA based, SMC and STA approaches

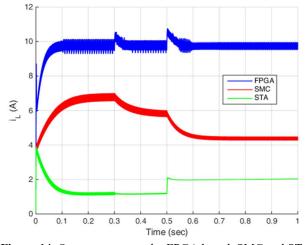


Figure 14. Output current under FPGA based, SMC and STA approaches

The points that can be seen from Figure 14 are as follows: The phenomenon of chattering is inevitable in the results obtained from all three used control approaches, and according to the system structure and extreme changes in the load, the chattering phenomena, overshoot/undershoot and rapid changes in the signals are observed. Also, only planned technique is capable of tracking the reference current $i_l = 9.74 A$, and other approaches are not able to perform this tracking and ensure stability and getting optimal performance due to the nature of the converter and also the effects of uncertainty.

The output voltage changes of the boost converter by applying the planned FPGA based, SMC and STA approaches are shown in Figure 15. As it is clear from this figure at different moments, the output voltage of the converter in the FPGA-based method is about 1.7 times the SMC technique and 2.6 times the STA scheme. The changes in the output voltage of the boost converter according to the changes in the output load achieve the maximum power of the converter fuel cell structure are shown in Figure 16. The figure shows power extraction more than 2 times in FPGA based method compared to SMC technique and more than 8 times compared to STA scheme which of course can be justified by tracking the optimal load current by the planned technique and considering the output voltage diagram of the converter in Figure 15. As it is clear, despite the extreme load changes, the planned FPGA based hybrid scheme achieves the maximum power in the shortest possible time, and its robust performance is shown in Figure 16. The STA and SMC approaches are not able to provide the maximum power that can be extracted from the fuel cell system due to their inability to track the optimal current, and as a result, they cannot guarantee the robust and optimal performance of the system.

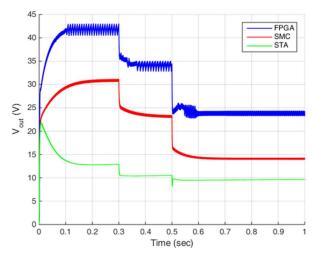


Figure 15. The convertor output voltage under FPGA based, SMC and STA approaches

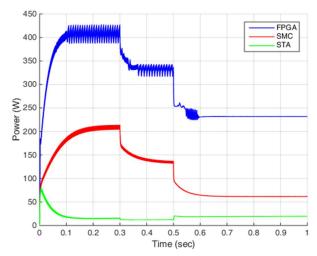


Figure 16. The converter output power under FPGA based, SMC and STA approaches

5. CONCLUSION

Considering the importance of energy and the need to extract maximum power from renewable sources, a new hybrid control method for PEMFC system with DC/DC boost converter was presented in this study. Considering the nonminimum phase state of the converter as well as the uncertainty of the model, a new indirect hybrid control structure was planned using adaptive, sliding mode and PI techniques to extract the maximum power and its practical implementation was done by using FPGA user interface with MATLAB. Despite the non-minimum phase nature of the converter, the simulation results showed the ability to accurately track the optimal load current and, as a result, extract the maximum power from the fuel cell system, overcome the effects of uncertainty, and show the robust and optimal performance by applying the planned scheme. Considering the output power distortions under the planned hybrid technique, which is a weakness for the present study, the power extracted from the fuel cell system using the FPGA technique can be increased to more than twice the power under the previous procedures. Future studies will include the design of other robust control approaches to overcome the effects of chattering, considering the limitations of the control signal and cover the effects of disturbance and noise.

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