

Graphene-based derivative as interfacial layer in graphene/n-Si Schottky barrier solar cells

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ABSTRACT

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In Schottky barrier solar cell (SBSC), the interface between absorber and front electrode plays a vital role for reducing the dark current, blocking the majority carriers injected into the electrode at forward bias, reducing surface recombination and passivating the silicon surface. In this respect, the addition of interfacial layer between the semiconductor absorber and the metal electrode can reflect into an improvement of the device performance.

Here we combine n-type crystalline silicon with stacks of graphene and graphene-based derivative (GBD) layers with different properties, in order to realize efficient SBSCs. Graphene layers with different structure, work function and electrical conductivity, were obtained by varying the chemical vapor deposition (CVD) parameters: conductive graphene films were grown at 1070 °C, GBD interfacial layers at 790 °C. The stacked structures were fabricated by the multiple transfer of these films. The films and the stacks were characterized by Raman spectroscopy. The device with the GBD interlayer (acting as hole transport layer) exhibits promising performances in terms of external quantum efficiency (EQE) and power conversion efficiency (PCE, ~5 %). Doping treatments with nitric acid vapor was performed and improved the cell PCE up to 6.7 %.

1. INTRODUCTION

In the last few years, a great deal of interest has been focused on the graphene-on-silicon (Gr/Si) Schottky barrier solar cell cells (SBSC) [1–3]. The recent progress has shown the potential to produce low cost and high efficiency solar cells with this configuration. In Gr/n-Si SBSC the graphene not only serves as a transparent conductive electrode but also contributes as an active layer for carrier separation and hole transport [4–8]. The Gr/n-Si solar cells can be fabricated by simply transferring a graphene film onto a n-Si substrate at room temperature. Their fabrication is thus much less expensive and easier in comparison to traditional Si solar cells based on p-n junctions. The first Gr/n-Si solar cell achieved a power conversion efficiency (PCE) of 1.5 % [4], but within five years this value reached 15.6 % [9], thanks to various improvements, such as graphene’s chemical doping [10–14], the use of multilayered graphene films [15–16], the application of antireflection coatings or light-trapping layers [12–13, 17–19], and the engineering of the Gr/n-Si interface [9, 20–22]. Among these various approaches, the chemical doping has been considered the most effective method to enhance the performance of Gr/n-Si solar cells. For instance, it has been reported that the PCEs of Gr/n-Si solar cells were improved to ~9 % after doping the graphene films by bis(trifluoromethanesulfonyl)amide (TFSA) [10] and nitric acid (HNO₃) [17–18]. The PCE of a HNO₃-doped Gr/n-Si solar cell can increase if combined with the application of an antireflective coating [17, 18]. Nevertheless, the PCE of these

cells are still much lower than that of state-of-the-art crystalline Si solar cells. The performance of Gr/n-Si SBSC is highly affected by the recombination of the charge carriers at the interface due to a low Gr/n-Si Schottky barrier height (SBH) (~0.6–0.7 eV), much smaller than the traditional silicon solar cells, which causes a large leakage current and thus a low open circuit voltage (V_{oc}) [1]. Among the different approaches to reduce charge recombination and improve the performance of Gr/n-Si SBSC, one method consists in engineering the interface between graphene and Si by adding interfacial layers. Such layers can play a key role in suppressing the charge recombination at the interface and improving the V_{oc} of the cell. A high PCE (15.6 %) for Gr/n-Si SBSC has been achieved by optimizing the thickness of the native oxide interfacial layer [9]. The thin native oxide layer (~2 nm) acts as a passivation layer, reducing the influence of surface defects and the reverse dark saturation current, thus improving the V_{oc} . To address this issue, it was proposed to insert an insulating layer between graphene and Si, thereby forming a metal-insulator semiconductor (MIS) structure [6]. In such MIS configuration, the additional insulating layer works as an electron blocking layer preventing the diffusion of electrons (majority carriers) from n-Si to graphene and thus reducing the carrier recombination. An appropriate band alignment between the insulating layer and the n-Si is also required to reduce the effect of hole (minority carrier) transport from n-Si to graphene. Furthermore, the insulating layer should be very thin (down to atomic thickness) and uniform to avoid increasing the series resistance (R_s). SBSC with optimized

aluminum oxide (Al_2O_3) [23] or hafnium oxide (HfO_2) [24] interfacial layers have been reported to achieve stable, high-efficiency Gr/n-Si junction. It has been reported that the introduction of graphene oxide (GO) [20, 25] or organic polymer film [12, 26] at the Gr/n-Si interface could effectively suppress the interface recombination of Gr/n-Si solar cells and increase the V_{OC} . 2D materials such as MoS_2 monolayer [21, 27] and hexagonal boron nitride (h-BN) [28] have been also investigated as effective electron-blocking/hole-transporting layers.

In the present work, we report on the use of a graphene based derivate (GBD) as interlayer, realized by chemical vapor deposition (CVD) of ethanol on copper. Such as the top graphene electrodes, the GBD interlayers are transferred onto silicon at room temperature, thus at lower temperature than the conventional insulating SiO_x layers. We characterized graphene films with different structural and electrical properties. SBSC with varying number of graphene and GBD layers were fabricated by using cyclododecane-assisted transfer [29-30], as shown previously for Gr/n-Si SBSC [17] and organic solar cells [31].

SBSC with single and double GBD interlayers were tested and the results have been compared with a standard SBSC without interfacial layers. Single-interlayer Schottky junction showed an increase of SBH and a decrease of R_s and ideality constant (η), confirming the improvements of junction reducing the recombination sites at the interface. A molecular doping treatment (by nitric acid vapors) was performed on the solar cells with single interlayer. The effects of graphene molecular doping on Gr/GBD/n-Si heterojunction solar cell performances have been investigated. After the treatment, the cell's PCE increased from 4.8 % to 6.7 %.

2. EXPERIMENTAL SECTION

2.1 Sample preparation

Conductive graphene (few layer graphene or thin graphite) and GBD interlayer were grown by ethanol CVD onto the copper substrates respectively at 1070 °C and 790 °C, using the CVD parameters reported in previous works [31-32]. The CVD reactor consists of a cold-wall chamber, made of a quartz tube equipped with an inductively coupled graphite susceptor heater (Figure 1). The heater is excited by a 3 kW (maximum power) radio frequency current source, which is modulated by the signal of a thermocouple buried inside the graphite susceptor. This system presents various advantages over classic CVD configurations: it does not entail the heating of the quartz tube and/or the entire CVD system [33], it allows controlled and fast heating and start-up time for the whole system and the possibility to reach higher temperatures than with more conventional, coaxial quartz tube furnaces.

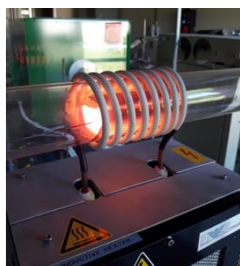


Figure 1. The inductively heated CVD reactor

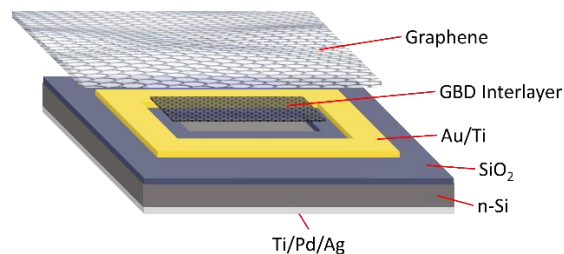


Figure 2. Schematic illustration of the 1L-SBSC

After growth, both kind of layers were transferred onto the cell substrates and after each transfer the cell was heated for 60 min at 60°C to help cyclododecane removal, and then to 90°C for 20 min for final drying. Different SBSCs were fabricated with stacks of two or more layers, with graphene as electrode and GBD as interlayer between graphene and n-silicon. The same transfer procedure was applied for transferring each layer onto target substrates for characterizations. For the solar cell fabrication, polished Si substrates ([100]-oriented, n-doped, 1 Ω cm), with thermally grown SiO_2 layer (300 nm) were patterned by photolithography and wet-etching of the oxide (by hydrofluoric acid solution) to prepare square windows with an active area of 0.09 cm^2 . The scheme of the solar cell is shown in Figure 2. The front and back contacts were realized by evaporating Ti/Au on the SiO_2 and a Ti/Pd/Ag trilayer on the back side of the n-Si, respectively [17]. The GBD was interposed between conductive graphene and silicon to work as an interlayer in the Schottky junction. The GBD covered the active area, but it was removed from the Au/Ti front electrode; instead, the top graphene layer entirely covered the Au electrode. Two kinds of SBSC devices were fabricated with different top architecture: 1) single-layer GBD between single-layer graphene and n-Si (1L-SBSC), and 2) double-layer GBD between double-layer graphene and n-Si (2L-SBSC). SBSCs based on standard Gr/n-Si junction were also fabricated for comparison. Molecular doping was performed by exposing the top part of the cell to HNO_3 vapor (from a 70 % solution diluted 1:1 in deionized water) at ambient conditions for 3 min. Details of the doping process can be found in ref. [34].

2.2 Sample characterization

The sheet resistance of the graphene was measured by four-point probe (NNPSON RESISTAGE RG-8) method in Van der Pauw configuration. Raman spectra were acquired on graphene and GBD films after the transfer onto Si/ SiO_2 substrates. Raman scattering measurements were carried out at room temperature with a HORIBA Scientific LabRAM HR Evolution Raman spectrometer with an integrated Olympus BX41 microscope. Laser excitation wavelength of 532 nm (2.33 eV) was focused on the sample surface using a 100 \times objective with a spot size of approximately 1 μm . Low laser power (below 1 mW) was used which minimized sample heating and possible damages. The solar cells were characterized by means of external quantum efficiency (EQE) and current density–voltage (J-V) measurements. EQE measurements were carried out with a Bentham PVE300 apparatus (Reading, U.K.) calibrated with a Si detector and using a probe light with a spot size much smaller than the cell area.

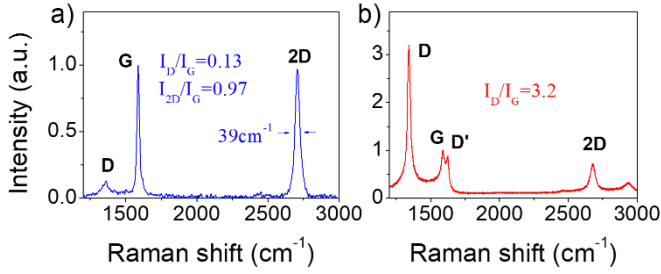


Figure 3. Raman spectra acquired on each layer of the solar cell: a) conductive graphene, b) GBD interlayer

Illuminated J-V characteristics were performed with a Keithley 228a voltage/current source (Keithley Instruments Inc., Cleveland, USA) and HP 3478A multimeter measure unit (Palo Alto, USA). White light illumination was provided by a class AAA solar simulator from WACOM (model WXS-155S-L2) equipped with a 1000 W Xenon lamp and a 400 W Halogen lamp. The light intensity was calibrated using a mono-Si reference cell in standard test conditions (25° C, AM1.5G, 1000 W/m²).

3. RESULTS AND DISCUSSION

3.1 Structural characterization of graphene and GBD layers

Raman spectra of the graphene and GBD interlayer are reported in Figure 3. The Raman spectrum of graphene (Figure 3a) exhibits the characteristic D, G and 2D peaks respectively at ~1350 cm⁻¹, ~1580 cm⁻¹, ~2700 cm⁻¹ [35]. Commonly the D to G (I_D/I_G) and 2D to G (I_{2D}/I_G) intensity ratios provide quantitative information on defect density and thickness of graphene, respectively [32, 36]. $I_{2D}/I_G < 1$ and $I_D/I_G \sim 0.13$ measured values indicate the formation of multilayer graphene [35, 37] with low defect density [38]. The Raman spectrum of the GBD is shown in Figure 3b. The main features of a graphene spectrum are still present, but in this case the D peak strongly intensifies and the defect-related D' peak at ~1620 cm⁻¹ appears [31]. The high value of I_D/I_G intensity ratio (~3.2) is typical of defective carbonaceous film [37]. A similar very sharp D peak was already observed in functionalized graphene [39], and in hydrogenated graphene grown at 650° C by plasma-CVD [40]. The sheet resistance of the graphene and GBD films are 0.5 and 124 kΩ/sq, respectively [31]. The charge separation and transport in the 1L-SBSC can be understood from the energy band diagram in Figure 4.

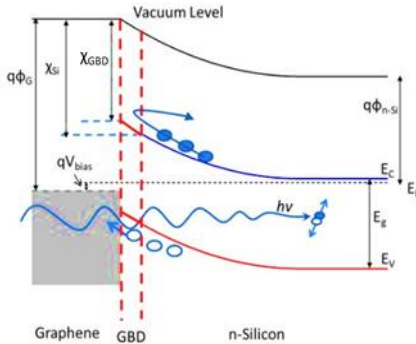


Figure 4. Schematics of band diagrams for 1L-SBSC

For the GBD interlayer, the valance band maximum is 4.9 eV and the absorption in the UV leads to an estimated optical gap of ~ 5.1 eV (as reported in previous work using the same GBD for organic solar cells [31]), while n-Si has the conduction band minimum and valance band maximum of 4.05 and 5.17 eV, respectively. Upon irradiation, electron-hole pairs generated in Si would diffuse across GBD/n-Si interface and then be separated by the built-in electric field of the heterojunction.

3.2 Solar cell characterization

Electrons in the Si conduction band are preferentially collected by the Ti/Pd/Ag electrode (cathode), while injection of electrons from Si to graphene anode is prevented by the interlayer due to the offset between the two conduction bands. On the other hand, holes are readily injected into the valance band of interlayer because of the negligible offset between the two valance bands, and then collected by the graphene anode. Therefore, the interlayer not only acts as the hole transport layer, but also serves as an electron blocking layer for reducing the carrier recombination at the anode. This should lead to a lower saturation current density and hence a larger open circuit voltage for the device.

Devices with single and double GBD interlayers were tested. Figure 5a shows the dark $\ln(J)$ -V characteristic of 1L- and 2L-SBSC compared to SBSC. The reverse saturation current density of SBSC is estimated to be 8.9×10^{-4} mA/cm² while for 1L-SBSC it decreases to 4×10^{-4} mA/cm². An increase to 3.8×10^{-3} mA/cm² is measured in the 2L-SBSC. This hints that the carrier recombination is reduced with the introduction of single GBD layer. On the contrary, the increase of 2L-SBSC reverse saturation current density would indicate the presence of a large quantity of trap states between stack interfaces. The SBH can be evaluate by analyzing the dark J-V characteristic. The diode characteristics of Schottky junction is described by thermionic emission theory of majority carriers over the Schottky barrier according to equation [10]

$$J = J_s \left[\exp\left(\frac{qV}{\eta k_B T}\right) - 1 \right] \quad (1)$$

where η is the ideality factor, k_B is Boltzmann's constant ($k=8.62 \times 10^{-5}$ eV/K), T is the temperature in Kelvin, q is the electronic charge (1.6×10^{-19} C) and the saturation current density J_s is described by the equation

$$J_s = A^* T^2 \exp\left(-\frac{q(SBH)}{k_B T}\right) \quad (2)$$

where A^* is the effective Richardson constant.

Solar cell SBHs, evaluated by taking the slope at the forward bias linear region of $\ln(J)$ -V curve (Figure 5a), are summarized in Table 1. The non-linearity observed in the lower bias part of the forward curve of 2L-SBSC is due to the presence of leakage currents. Such current components are usually attributed to generation and recombination of carriers in the charge space region, field emission and thermionic field emission or surface/edge effects that may lead to local barrier lowering [41, 42]. The η was also extracted from $\ln(J)$ -V curve and it is found that it is reduced from 2.15 to 1.6 when a single layer of GBD is inserted in the junction, but it reaches 3.5 value in 2L-SBSC.

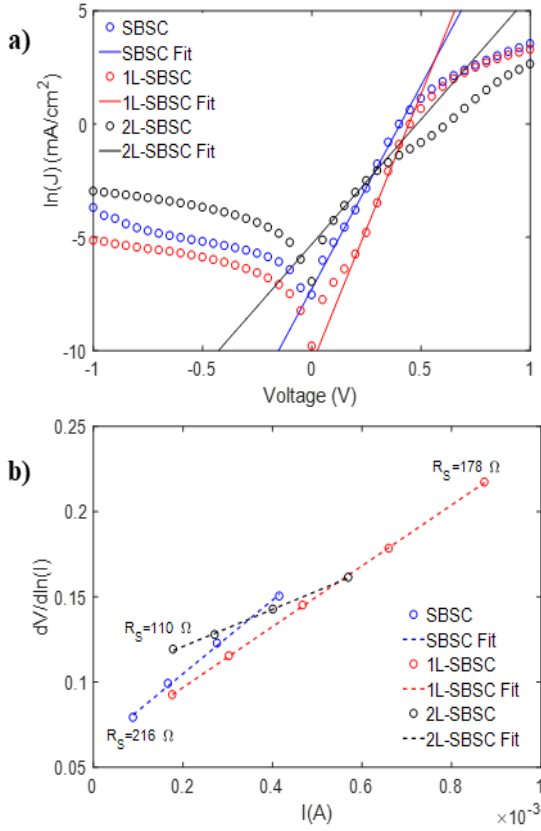


Figure 5. (a) Dark $\ln(J)$ -V characteristics and (b) Plots of $dV/d\ln(I)$ versus I for SBSC (blue), 1L-SBSC (red) and 2L-SBSC (black curve)

Typical values for η in undoped graphene/n-Si diodes are in the 1.6-2.0 range [10]. The properties of the junction are strongly affected by unwanted contaminants or defects due to the fabrication process, which acts as charge traps and add interface states. These defects and contaminants cause charge accumulation and induction of charge puddles in graphene, resulting in local variation of the SBH. The spatial inhomogeneity of the barrier contribute to the high η [43]. Accordingly, the high values of the measured ideality constants are associated with the existence of impurities on the graphene and on the GBD layers that are unintentionally formed during the fabrication process and which give rise to associated Schottky barrier inhomogeneities. Only for 1L-SBSC η decreases and this reveals a lower interface recombination and hence better junction quality. Figure 5b shows $dV/d(\ln I)$ versus I plot, from

$$\frac{dV}{d(\ln I)} = \frac{\eta k_B T}{q} + R_s I \quad (3)$$

and then it is possible to extract the R_s value from the slope of the curve linear fitting [44]. As also reported in Table 1, R_s is reduced from 216 Ω to 178 Ω for 1L-SBSC, and to 110 Ω for 2L-SBSC.

Table 1. The diode characteristics of Schottky junctions reported in Figure 5

Device	SBH(eV)	η	R_s (Ω)
SBSC	0.78	2.15	216
1L-SBSC	0.87	1.60	178
2L-SBSC	0.70	3.50	110

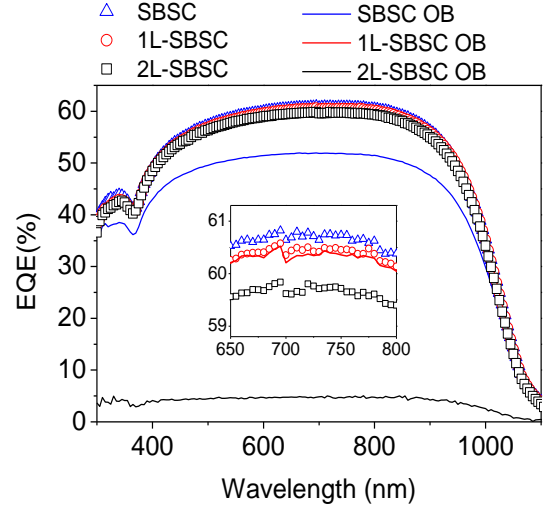


Figure 6. EQE curves without and with OB of SBSC (blue), 1L-SBSC (red) and 2L-SBSC (black)

Additional characterization is presented in Figure 6, which shows the EQE of the SBSCs acquired with and without optical bias (OB). Without OB all devices showed an EQE $\sim 60\%$ in the wavelength range $650 \text{ nm} < \lambda < 800 \text{ nm}$ (Figure 6, inset), a value in line with the state of the art of Si solar cells [10]. The EQE curves acquired with OB result unchanged in the case of 1L-SBSC, indicating a significant electron-hole pair generation, separation and collection by the corresponding electrodes. On the other end, the EQE reduction in OB condition observed in the case of SBSC and of 2L-SBSC, indicates an activation of recombination centers responsible for the entrapment of photogenerated carriers. Since the photogeneration for the device without and with the interlayer is identical, the higher EQE observed for the 1L-SBSC with OB is due to more efficient charge separation and charge collection as a result of increased SBH and reduced recombination centers at the interfaces, which also reflect in reduced R_s . Probably, in the 2L-SBSC case, the wet fabrication process and film transfer procedures introduce some contamination at the interfaces and these defects then increase the recombination centers, reducing the EQE measured in OB condition.

3.3 Doping treatment

The J-V characteristics under illumination were acquired on the 1L-SBSC that showed the better performances in term of EQE in our experiments. The curves were acquired just after the fabrication (pristine cells) and after each doping process step (HNO_3 doping, ageing and recovery), as already reported for Gr/n-Si SBSC [17]. The curves in Figure 7 show the effect of the different treatments on the behavior of 1L-SBSC. The relative electrical parameters are reported in Table 2. In Figure 7, the illuminated J-V characteristics clearly evidence the effect of doping on cell performance. The undoped cell shows a $V_{oc} \sim 0.52 \text{ V}$ with a R_s of 17.5 Ω . The molecular doping improves the cell performance in particular in term of PCE and fill factor (FF), while reducing the R_s . The FF increases from 39.4 % to 54.2 %. The result is an increase of PCE from 4.8 % of the pristine cell to 6.7 % of the doped device. The R_s decreases from 17.5 to 7.8 Ω . The influence of doping on the short-circuit current density (J_{sc}) is negligible, as already observed in literature [11, 45].

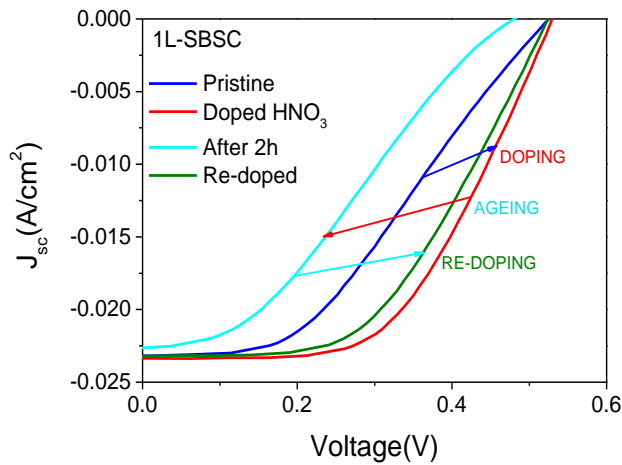


Figure 7. Doping, ageing and recovery effect on illuminated J-V curve of 1L-SBSC: before the doping (blue), immediately after doping (red), after 2 hours (light blue) and after re-doping process (green)

The improvements in the cell photovoltaic parameters by HNO_3 treatment can be attributed to different factors. The molecular doping is expected to decrease the sheet resistance of graphene [11, 14] and this leads to a proportional decrease in the R_s of the solar cell, and to a concomitant increase in the FF. Beside the significant decrease in the graphene sheet resistance, a volatile oxidant treatment such as the exposure to HNO_3 vapor is expected to improve the uniformity of the Schottky junction by saturating defects at the interfaces. This should also contribute to decrease the cell R_s , further increasing its FF [11]. The light blue curve in Figure 7 shows the ageing effect on the cell. After 2 hours, the photovoltaic parameters show a worsening. Ageing degrades the V_{oc} (reaching 0.48 V) and FF parameters and PCE decrease respectively at 32.7 and 3.5 %, increasing the S shape of the curve with the R_s that measures 32.3 Ω . This effect is due to the instability of the doping obtained by volatile acid compounds which causes lower SBH and hence decreases the value of V_{oc} [11] as also reported for simple junction Gr/n-Si [17]. A quasi-complete recovery of photovoltaic parameters is possible upon repeating the exposure to HNO_3 (green curve in Figure 7).

4. CONCLUSION

SBSC based on few-layer Gr/n-Si junction with single and double GBD interlayers were fabricated and characterized. J-V curves and EQE confirm that a single GBD layer improves the Schottky junction, by reducing the inhomogeneity at interface which acts as charge traps and add interface states and recombination centers. In 1L-SBSC, the saturation current and R_s are lowered, while the SBH increases from 0.78 to 0.87 V in comparison with SBSC. η was reduced from 2.15 to 1.6, as indicator of reduction of interface recombination centers. The EQE curves confirm the improvements derived by the insertion of single GBD interlayer in Gr/n-Si junction, in particular for measurements acquired in OB conditions. 1L-SBSC showed an EQE with a ~ 20 % enhancement compared to SBSC. Doping treatment with HNO_3 vapor (which p-dopes graphene) was performed on the 1L-SBSC, whose PCE reached 6.7 %. The degradation of the solar cells parameters measured after 2 hours of storage in air has been completely

recovered by re-exposing the cell to HNO_3 .

Table 2. Photovoltaic parameters calculated from curves reported in Figure 7

Process step	J_{sc} (mA/cm ²)	V_{oc} (V)	FF (%)	PCE (%)	R_s (Ω)
Pristine	23.1	0.52	39.4	4.8	17.5
Doped HNO_3 3min	23.3	0.53	54.2	6.7	7.8
After 2h	22.6	0.48	32.7	3.5	32.3
Re-doped HNO_3 3min	23.2	0.52	50.7	6.2	9.0

In the present work we highlight that the insertion of GBD interlayers can improve the interface between n-Si and conductive graphene, thus increasing the PCE. This work sets the basis for the exploration of solar cells fabricated with various absorbers and stacks of GBD films with selected properties, such as layer number, workfunction, charge transport behavior, and doping level.

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