



Power Density Enhancement of Three-Phase Rectifier Using Higher Frequency Solid State Transformer

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ABSTRACT

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This article introduces an isolated three-phase six-pulse rectifier using a solid-state transformer. The line frequency utility grid voltage is modulated by a frequency boost converter. This converter exists two parts. The first part is the rectifier section, and the second one is the inverter section. Then after, three-phase diode rectifier is powered by the medium frequency transformer 2 kHz to shrink the transformer size upto 20% or even 10% depending to the frequency operating range for silicon steel core materials. The aim of this study is to shrink the occupied size of the rectifier using a higher frequency transformer. The study realizes that the grid line current has the same frequency contents as a regular three phase rectifier. In other words, the presented idea results in smaller weight and size while sustaining the performance of input current total harmonic distortion THD=31%. High power density power electronics converter would be extremely valuable for applications such as electric vehicle railways and aircraft. The proposed approach is reliable and easy to use. The solid-state transformer contributes to isolating and improving the overall converter's power density. Simulation results for the 16 kW converter were verified using MATLAB Simulink.

1. INTRODUCTION

Nowadays, worldwide the demand on DC power supplies is increasing rapidly, especially for automotive, railway, aircraft, and data center applications. The value of the global semiconductor rectifiers market is predicted to hit \$6.6 billion by 2026 [1]. Three phase conventional diode rectifier is one of the most trusted and popular converters due to its high reliability and durability [2-6]. However, the conventional three-phase rectifier utilizes conventional line frequency transformer (50/60) Hz for isolation purposes. The use of a conventional transformer has a negative consequence on the total power density of the three-phase rectifier. The power density is given by Eq. (1) [7]. It is ratio of the total power process through a converter to the total volume occupied by that power converter. Figure 1 illustrated the relationship between the occupied by transformer's core with its running frequency [7]. High power density implies less footprint of power electronics converters [8]. For automotive and aircraft applications, high power density means reduced transport fuel consumption. Magnetic components such as transformers and inductors have the major footprint for the power electronics converter [9]. A higher power density is achieved by increasing the transformer's running frequency. Despite its smaller core cross-section area, the transformer core still produces the same amount of flux lines. Figure 1 shows A 1MVA silicone steel (Si) core transformer's volume shrinks from (2000 to 200) liter if the transformer's core running frequency is raised from (50 to 1 kHz) respectively [7].

Renewable energy resources such as solar and wind turbines are the most promising energies worldwide. For more than a century, transformers have formed the backbone of electrical grids. However, as the world seeks to address the issue of climate change by expanding the utilization of renewable energy sources such as wind turbine farms and solar photovoltaic stations, new developments in transformer technology will play a significant role in this effort. In ocean depths where fixed-foundation turbines are impractical, floating wind farms, which comprise of wind turbines installed on floating structures, are ideally suited. They may vastly expand the amount of ocean that can be used for offshore wind farms across the world. If a wind farm has to have transformers, the best place for them to be inside the wind turbine tower, as shown in Figure 2 [10].

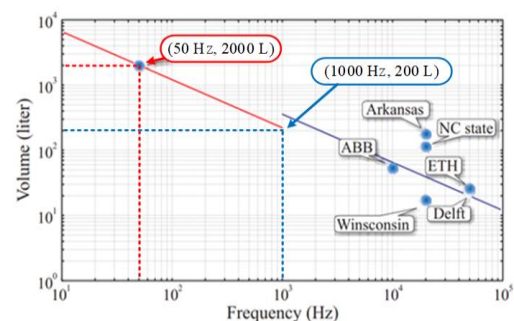


Figure 1. Transformers' core occupied with operation frequency [7]

The U.S. Department of Energy presented a vision 20% wind energy by 2030 to motivate the developing wind power [11]. Three-phase, six-pulse and multi-pulse converters are extensively used for high-power applications such as wind turbine and high voltage direct current HVDC [12-16]. However, the major drawback of these topologies is the low power density due to the use of a hefty line frequency transformer. Moreover, the traditional transformers such as the On-Load Tap OLTC Changing have some sort of low response [17].

$$Power\ Density = \frac{Total\ power\ process\ (w)}{Total\ volume\ occupied\ (Liter)} \quad (1)$$

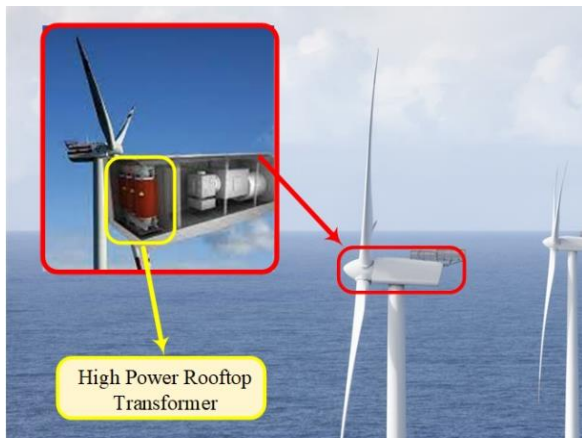


Figure 2. Off-shore rooftop wind turbine transformer proposed by ABB [10]

A solid-state transformer combines a power electronics converter with high-frequency transformer to perform voltage transformation, isolation, and extra regulation. The use of solid-state transformers reduces the hefty of magnetic components and allows for finer levels of control. However, these functionalities are unavailable from line frequency transformers. The solid-state transformers have become more popular due to the use of advanced power semiconductors such as Silicon Carbide SiC and Gallium Nitride Gan. Many applications can be benefited from solid-state transformers because of their adaptability and ease of control [18-23]. General Electric GE proposed and designed 1 MVA, 20 kHz solid state transformer. Their proposal was compared with a regular transformer in size and weight. The proposed is illustrated in Figure 3 [24]. The outcome results proved better power density for their proposal. In response to the power density concerns, the presented study presents an isolated six-pulse diode converter with the use of solid-state transformer, as shown in Figure 4.

The proposed idea indicates that the power density can be enhanced without sacrificing the performance of the six-pulse rectifier. In this paper, a design example for three phase six-pulse rectifier using a high frequency (2 kHz) solid state transformer is discussed. Extensive Matlab simulation analysis is explained in details. The presented concept offers advantages as listed below:

- The presented topology is robust and performed as a regular isolated three-phase rectifier.
- The input utility current has the same quality as a regular six-pulse rectifier.
- Using a frequency boost converter can give more resilience to control the power flow.



Figure 3. General Electric prototype comparison between solid-state transformer and traditional one [24]

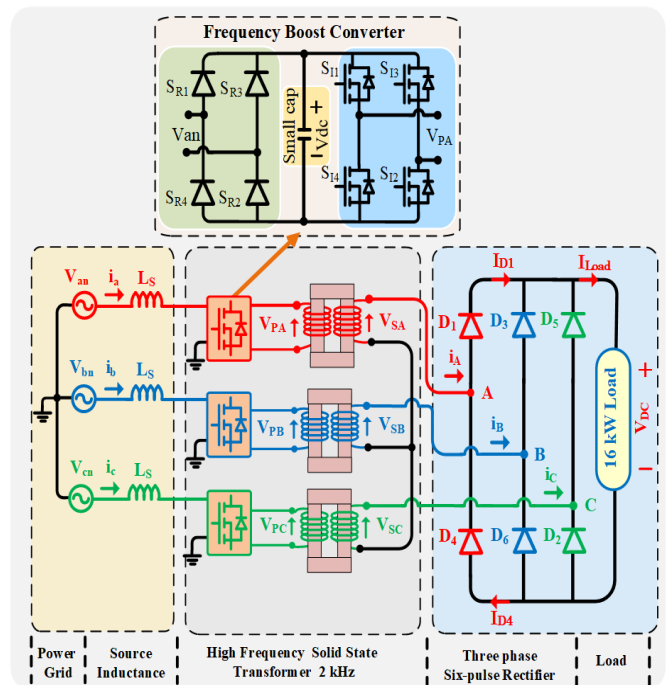


Figure 4. Proposed concept of three phase six-pulse rectifier using high frequency solid state transformer

2. METHODOLOGY

The proposal for a three-phase six-pulse rectifier with a high-frequency solid state transformer is seen in Figure 4. There are three sections of the suggested idea.

2.1 Frequency boost converter

Figure 4 shows the per-phase frequency boost converter FBC. Each converter involves a rectification section, an inversion section. The rectification part has a 50 Hz rectification function S_{Rec} , while the inversion part has a 2 kHz inversion function S_{Inv} , as shown in Figure 5. The converter input voltage frequency is 50 Hz, and its output voltage frequency is 2 kHz. The main job of this converter is to elevate the frequency voltage that would be linked to the transformer input side and process the power flowing from the grid to the six-pulse rectifier. For the second and third phases, their converter output voltages would be identical, but they would be displaced by 120° .

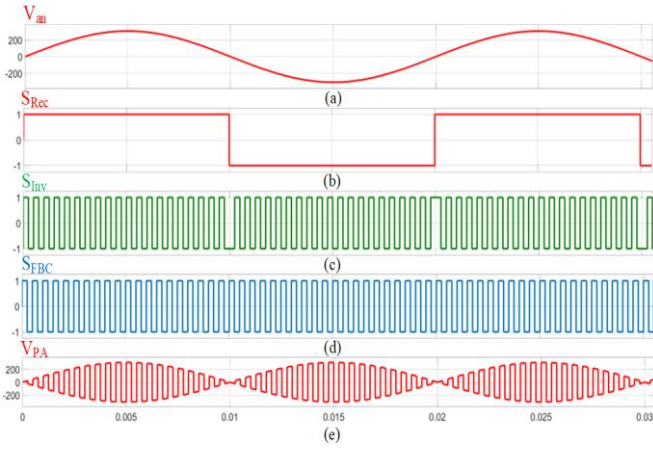


Figure 5. Frequency boost converter operating mechanism: (a) Primary line frequency (50 Hz) input voltage V_{an} ; (b) Rectification function S_{Rec} ; (c) Inversion function S_{Inv} ; (d) Function of frequency boost converter S_{FBC} ; (e) High frequency (2 kHz) primary voltage V_{PA} for phase ‘A’

2.2 Higher frequency isolation

The high-frequency transformer is the second portion of the proposed system. This transformer has been connected in star for both the main and secondary ends (see Figure 4). Core materials include silicon steel, amorphous, nanocrystalline, and ferrite, among others, could be used. However, the silicon steel core is the appropriate choice for high-power applications and optimally utilizes above the running frequency of 2 kHz, due to its cheap cost and acceptable core losses [24-31].

2.3 Three-phase diode rectifier

The last section of this proposal is a six-pulse diode rectifier wired to the secondary of higher frequency isolation, as demonstrated in Figure 4. It is preferable for each diode in the six-pulse rectifier to be used of silicon carbide due to its low loss.

3. GRID CURRENT MATHEMATICAL DERIVATION

The major harmonics content of electrical input currents (i_a , i_b , i_c) for the conventional six-pulse diode rectifier is well known (5th, 7th, 11th, 13th, etc.) [2, 4]. In this section, depth analysis and math derivation show that the current grid quality for the presented idea and a conventional six-pulse rectifier with the use of a regular transformer are totally identical. In order to derive mathematical equations for the input current i_a , the output load current I_{LDC} for the rectifier is assumed to be smooth DC (see Figure 6). The comprehensive mathematical analysis is applied to the first phase, as shown in Figure 6. I_{PA} is the primary transformer winding current, and S_{FBC} is the frequency boost converter switching function (see Figure 5). To acquire the relationship between I_{PA} and the secondary side currents of the transformer I_{SA} , the transformer volt-ampere balanced between the primary and secondary sides should be used, as explained in Eq. (2). From Figure 6, it can be observed that I_{SA} and I_{SB} as written in Eqs. (3) and (4):

$$i_a = S_{FBC} \times I_{PA} \quad (2)$$

$$N_{PA} \cdot I_{PA} = N_{SA} \cdot I_{SA} + N_{SB} \cdot I_{SB} \quad (3)$$

$$I_{SA} = I_{D1} \quad (4)$$

$$I_{SB} = -I_{D6} \quad (5)$$

Since the transformer turns ratio is one, so by substituting Eqs. (4) and (5) into Eq. (3) to get:

$$I_{PA} = I_{D1} - I_{D6} \quad (6)$$

The DC load current I_{LDC} goes through I_{D1} and I_{D6} , shaping the primary current I_{PA} as a quasi-square wave written by Eq. (7):

$$S_{quasi}(wt) = \sum_{n=1,5,7,11,13,\dots}^{\infty} \left(\frac{4 \times I_{LDC}}{n\pi} \cos\left(\frac{n\pi}{6}\right) \right) \sin(nwt) \quad (7)$$

The frequency boost converter’s switching function S_{FBC} is equal to S_{Rec} multiplied by S_{Inv} (see Figure 5 and Eqs. (8)-(10)). The w_{HF} is the boost frequency converter’s operating frequency 2 kHz:

$$S_{Rec} = \left(\frac{4}{\pi}\right) \sum_{n=1,3,5,\dots}^{\infty} \left(\frac{1}{n}\right) \sin(nwt) \quad (8)$$

$$S_{Inv} = \sum_{n=1,3,5,\dots}^{\infty} \left(\frac{1}{n}\right) \sin(nw_{HF}t) \quad (9)$$

$$S_{FBC} = S_{Rec} \times S_{Inv} = \left(\frac{4}{\pi}\right) \sum_{n=1,3,5,\dots}^{\infty} \left(\frac{1}{n}\right) \sin(n\pi D) \sin(nw_{HF}t) \quad (10)$$

Furthermore, I_{PA} has to respond to the whole switching function S_{FBC} . Due to the influence of S_{FBC} , the diode currents (I_{D1} , I_{D6}) would be written by Eqs. (11) and (12):

$$I_{D1} = S_{FBC} \times S_{quasi}(wt) \quad (11)$$

$$I_{D6} = S_{FBC} \times S_{quasi}\left(wt - \frac{2\pi}{3}\right) \quad (12)$$

Substituting Eqs. (11) and (12) into Eq. (6) gives:

$$I_{PA} = \left[S_{quasi}(wt) - S_{quasi}\left(wt - \frac{2\pi}{3}\right) \right] \times S_{FBC} \quad (13)$$

Substituting Eq. (11) into Eq. (2):

$$i_a = [S_{FBC}]^2 \left(\frac{2\sqrt{3} I_{LDC}}{\pi} \left[\sin(wt) - \frac{1}{5} \sin(5wt) - \frac{1}{7} \sin(7wt) + \frac{1}{11} \sin(11wt) + \dots \text{etc} \right] \right) \quad (14)$$

When the duty ratio D of the frequency boost converter is (50%), this implies:

$$[S_{FBC}]^2 = \left[\frac{4}{\pi} \sum_{n=1,3,5,\dots}^{\infty} \left(\frac{1}{n}\right) \sin(n\pi D) \sin(nw_{HF}t) \right]^2 = 1.0 \quad (15)$$

The phase ‘A’ input current i_a would be:

$$i_a = \frac{2\sqrt{3} I_{LDC}}{\pi} \left[\begin{array}{c} \sin(wt) - \frac{1}{5} \sin(5wt) \\ -\frac{1}{7} \sin(7wt) + \frac{1}{11} \sin(11wt) - \dots \text{etc} \end{array} \right] \quad (16)$$

Evidently, the harmonics content of the input utility current i_a for the presented topology, similar to the regular six-pulse rectifier.

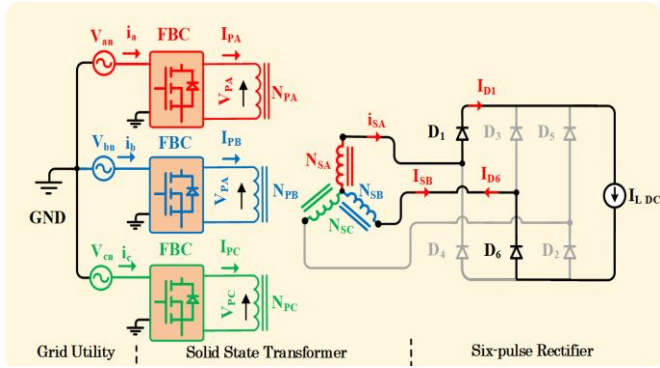


Figure 6. Input current analysis i_a for the presented topology in Figure 4

4. SIMULATION OF THE PRESENTED TOPOLOGY

The suggested concept has been simulated using Matlab

software in order to investigate the viability of the proposed system in Figure 4. The circuit parameters are listed in Table 1. The grid voltage is 380 volt line-to-line RMS value, 16 kW, 537 volt DC output voltage. Figure 5 explains the operating mechanism for the first phase frequency boost converter. The phase 'A' high-frequency voltage V_{PA} is linked to the input of high-frequency transformer. For phase 'B' and 'C', the V_{PB} , V_{PC} would be the same, but they are displaced by 120 degrees. Figure 7 shows the voltages, and currents for the transformer's primary side. The phase 'A' primary voltage V_{PA} Fourier series analysis is shown in Figure 8. The influential frequency is around 2 kHz. Clearly, the transformer works at 2 kHz. Due to the high-frequency operation of the transformer, definitely the overall power density would be increased. Figure 9 illustrates the grid utility side voltages, currents, and the grid current i_a 's THD. The frequency content for the utility input current i_a is shown in Figure 10. It has 5th, 7th, 11th, 13th which is same as a conventional six-pulse rectifier. Moreover, the total harmonic distortion THD for the input current i_a is 31% which is similar to a six-pulse rectifier using a conventional transformer. In other words, the performance of i_a is unaffected by the proposed idea, while the power density is increased due to the utilization of a solid-state transformer. The DC side load voltage VDC and current I_{Load} are illustrated in Figure 11.

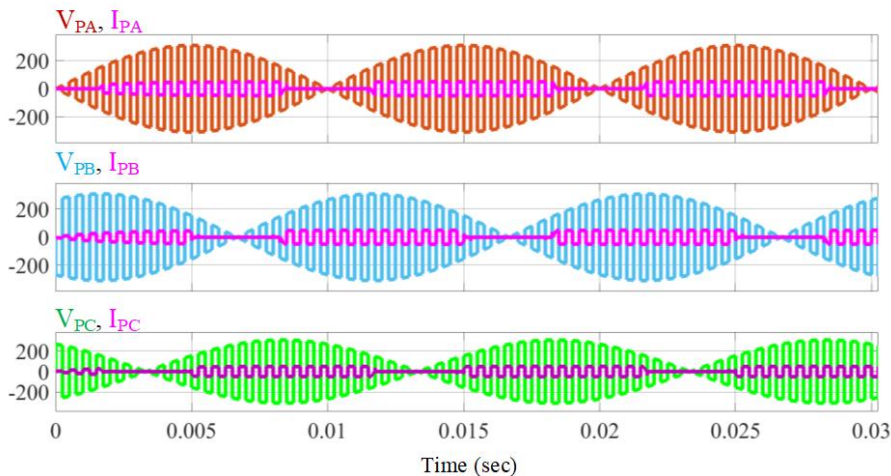


Figure 7. Voltages (V_{PA} , V_{PB} , V_{PC}), currents (I_{PA} , I_{PB} , I_{PC}) for the transformer's primary side

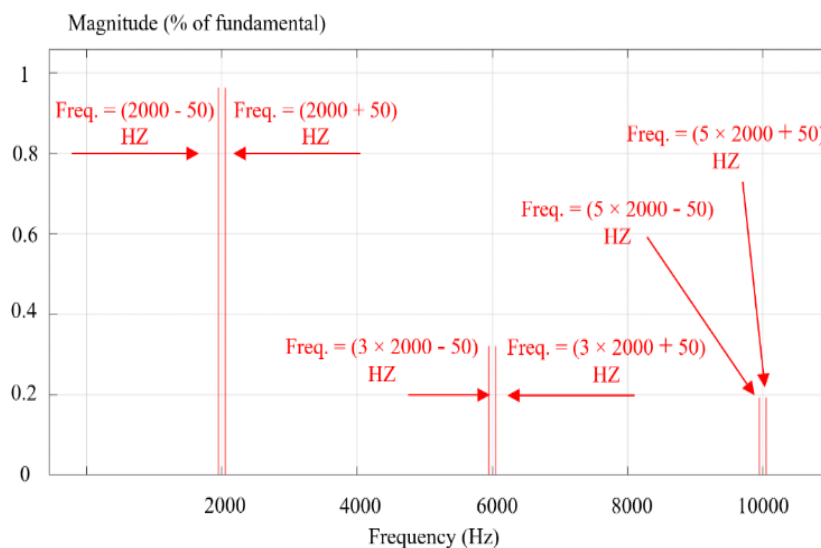


Figure 8. Fourier analysis for phase 'A' primary input voltage V_{PA}

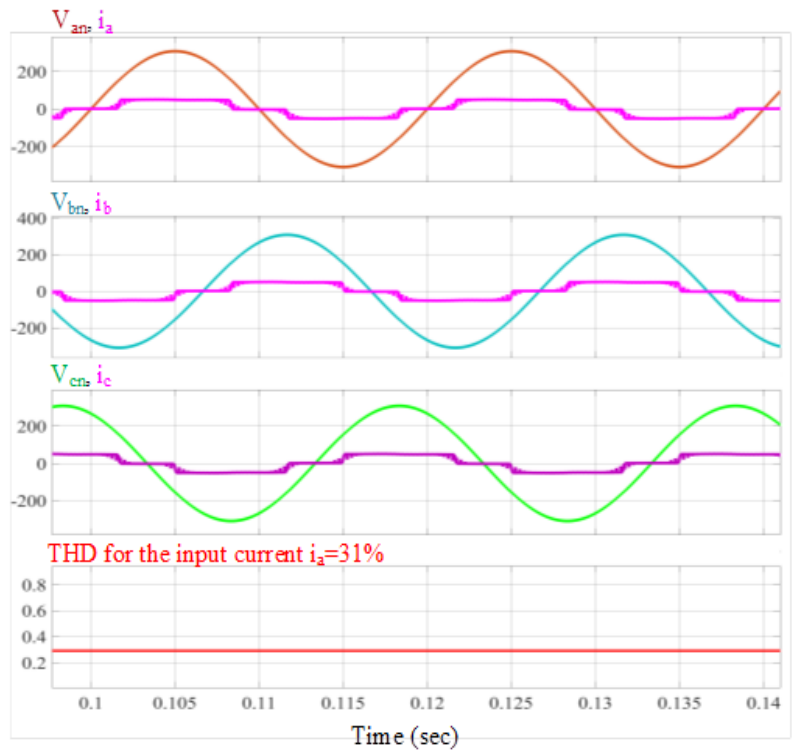


Figure 9. Grid utility voltages, currents (V_{an} , i_a , V_{bn} , i_b , V_{cn} , i_c), and the total harmonics distortion for phase 'A' input current i_a

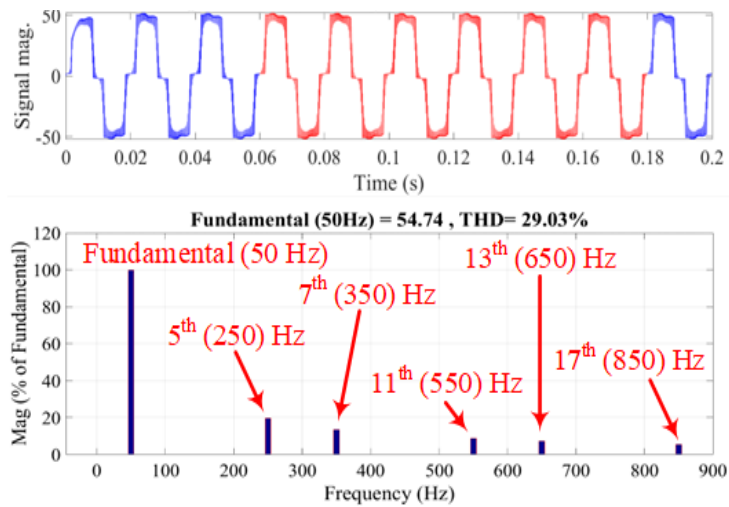


Figure 10. The frequency contains for the utility input current i_a

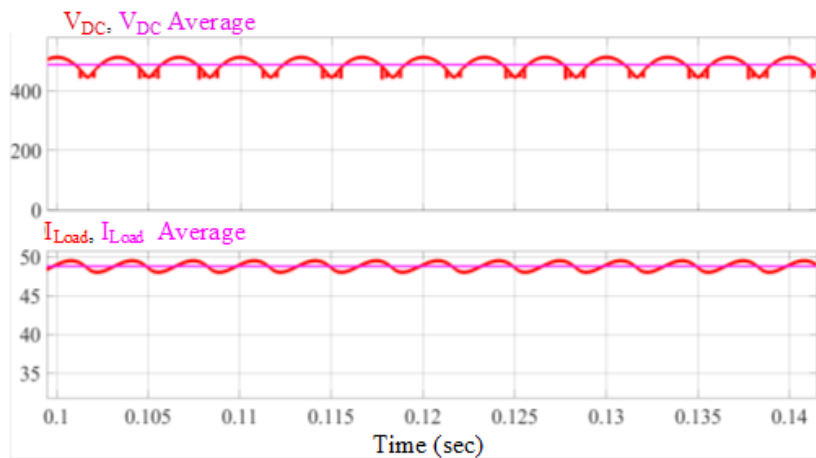


Figure 11. The DC side load voltage V_{DC} and current I_{Load}

Table 1. Design example circuit parameters for Figure 4

Utility Gird's Information	Transformer's Information	FBC Switches Parameters Rating
V _g =380 line-to-line RMs value	Core material: Silicon steel type	Rectifier bridge Peak current=52 A Peak voltage=311 V
Source resistance=26 mΩ	Operating frequency=2 k Hz	Inverter bridge Peak current=52 A Peak voltage=311 V
Source inductance=16.6 μH	Turns ratio=1:1	
Frequency=50 Hz		
Six-pulse diode rectifier		DC Load side's Information
Peak current=52 A		RL=10 Ω
Peak voltage=514 V		L=20 mH
Operating frequency=2 kHz		IL=52 A
		VDC=540 V

5. CONCLUSIONS

When the transformer works at line frequency (50/60) Hz, that tends to have negative consequences for the power density. The transformer's (volume/ weight) can be reduced by increasing its operating frequency. The higher frequency of operating, the higher power density. There will be a major role for recent developments in transformer technology. Floating wind farms, which consist of wind turbines installed on floating platforms, are well suited for offshore sites at ocean depths where fixed-foundation turbines are not feasible. If a wind farm has to use transformers, installing them in the turbine's tower is preferable. Solid state transformer could be the perfect solution to reduce the structure cost of the wind turbine tower and increase its reliability to achieve high efficiency for the total converter. The results of the presented idea demonstrate that the three phase rectifier's output DC voltage and utility input current quality are not affected by the usage of the solid-state transformer. However, the proposed approach swaps out the traditional line frequency transformer with a smaller solid state version, resulting in more power density for the isolated three-phase rectifier.

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